DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

INTERCONNECTION RESOURCES FOR PROGRAMMABLE LOGIC INTEGRATED CIRCUIT DEVICES

the specification of which

(check	[X] is attached hereto	
one)	[] was filed on	as
	Application Serial Noand was amended on	·
	(if applicable)	

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I do not know and do not believe that the invention was ever patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application.

I do not know and do not believe that the invention was in public use or on sale in the United States of America more than one year prior to this application.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known by me to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, \$ 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)		Priority Claimed
(Number) (Country)	(Day/Month/Year Filed)	[] [] Yes No
(Number) (Country)	(Day/Month/Year Filed)	Yes No

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

60/122,788 (Provisional Appln. No.)	March 4, 1999 (Filing Date)
60/142,431 (Provisional Appln. No.)	July 6, 1999 (Filing Date)
(Provisional Appln. No.)	(Filing Date)
(Provisional Appln. No.)	(Filing Date)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known by me to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior

application and the national or PCT international filing date of this application:

(Application Serial No.) (Filing Date)

(Status) (patented, pending, abandoned)

(Application Serial No.) (F	iling Date)	(Status) (patented, pending, abandoned)
Send correspondence to:	Robert R. Ja FISH & NEAVE 1251 Avenue New York, Ne	of the Americas ew York 10020-1104
Direct telephone calls to:	Robert R. Jo (212) 596-9	ackson 000
I hereby declare that all staknowledge are true and that a and belief are believed to be statements were made with the statements and the like so maimprisonment, or both, under United States Code and that jeopardize the validity of thereon.	true; and fe knowledge tade are punis	urther that these hat willful false hable by fine or of Title 18 of the
Full name of first inventor	James Schill	<i>(</i>
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : James Schleicher et al.

Serial No. : Not Yet Assigned

Filed : Concurrently Herewith

For : INTERCONNECTION RESOURCES FOR

PROGRAMMABLE LOGIC INTEGRATED

CIRCUIT DEVICES

Group Art Unit : Not Yet Available

Examiner : Not Yet Available

POWER OF ATTORNEY BY ASSIGNEE

Under the provisions of 37 C.F.R. § 3.71, the undersigned assignee of record, by virtue of an assignment submitted concurrently herewith for recording, of the entire interest in the aboveidentified patent application hereby elects to conduct the prosecution of the above-identified patent application to the exclusion of the inventors. assignee hereby revokes any powers of attorney previously granted and appoints Derek E. Minihane, Reg. No. 39,774, Robert R. Jackson, Reg. No. 26,183, and Jeffrey H. Ingerman, Reg. No. 31,069, its attorneys, with power of substitution, and with power of appointment of associate attorneys, and of revocation of their powers, to prosecute this application and any divisions, continuations in whole or in part, renewals and reissues of the same, and to transact all business in the Patent and Trademark Office connected therewith;

and assignee requests that communications be sent to:

Robert R. Jackson c/o Fish & Neave 1251 Avenue of the Americas New York, New York 10020-1104

and that telephone calls be directed to:

Robert R. Jackson
(212) 596-9000

Pursuant to 37 C.F.R. § 3.73(b), the undersigned hereby certifies that the evidentiary documents have been reviewed and, to the best of assignee's knowledge and belief, title is in assignee.

The undersigned hereby declares that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

ALTERA CORPORATION ASSIGNEE

Z/2*9*/2000 Date

By:

C. Wendell Bergere

Vice President, General Counsel and Secretary